193414US2

IN RE APPLICATION OF:

TATSUYA KUNIKIYO

· GROUP ART UNIT: 2814

SERIAL NO: 09/612,298

RCE FILED: NOVEMBER 1, 2002

: EXAMINER: CAO, P.

FOR: SEMICONDUCTOR DEVICE HAVING

MULTILAYER INTERCONNECTION

STRUCTURE

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated December 18, 2002, please amend this application as follows:

IN THE CLAIMS

Please amend Claim 1 as shown in clean form below. A marked-up copy of the

1. (Twice Amended) A semiconductor device comprising:

a semiconductor substrate having a main surface along which a semiconductor substrate having a main surface along which a semiconductor device comprising:

a semiconductor substrate having a main surface along which a semiconductor semicondu

element is formed:

amended claim is attached.

interlayer insulating films formed on said main surface;

conductive interconnections provided in a plurality of layers separated by said interlayer insulating films;